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Default operator: OR

Plurals

Highlight all hit terms initially

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRe	I
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5911051 A	19990608	72	High-throughput interconnect allowing bus	710/107		
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5974471 A	19991026	10	Computer system having distributed compression a	710/1	709/247	
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5977997 A	19991102	11	Single chip computer having integrated MPEG an	345/519	345/503;	
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6130680 A	20001010	21	Method and apparatus for multi-level demand cachin	345/544	345/503;	
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6249853 B1	20010619	25	GART and PTEs defined by configuration reqisters	711/206	345/538;	
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6353438 B1	20020305	27	Cache organization--direct mapp	345/552	345/540;	
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6667744 B2	20031223	35	High speed video frame buffer	345/502	345/556;	
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5974471 A	19991026	10	Codec logic for data transfer in computer syst		345/545;	
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6249853 B	20010619	25	Graphical address remapping table for compu		345/572	
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6353438 B	20020305	27	Accesssing method for tag information in memory use			